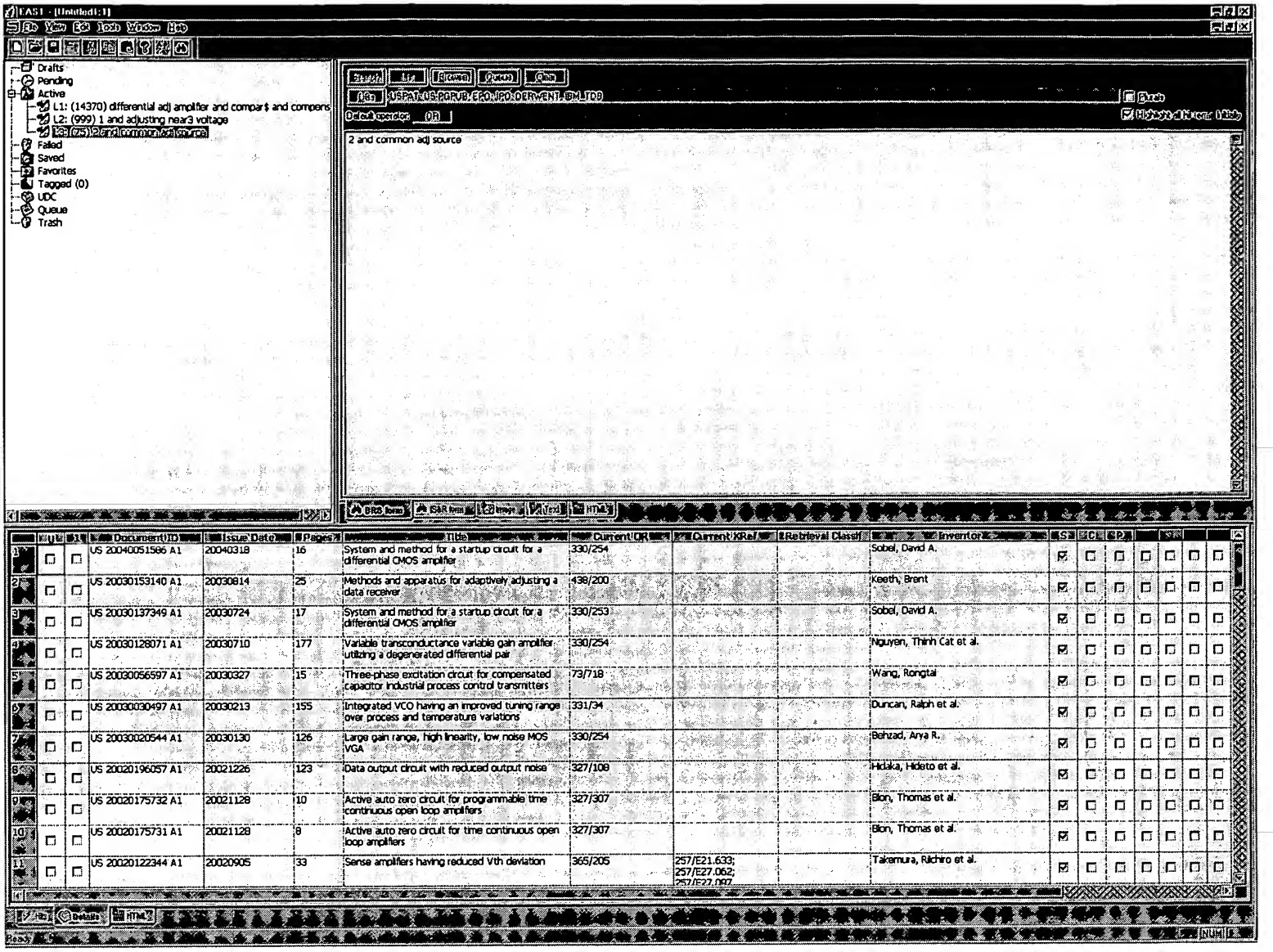

	Tag	#	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Classif	Inventor	S	C	P				
1	☐	☐	US 20040051586 A1	20040318	16	System and method for a startup circuit for a differential CMOS amplifier	330/254			Sobel, David A.	☑	☐	☐	☐	☐	☐	☐
2	☐	☐	US 20030153140 A1	20030814	25	Methods and apparatus for adaptively adjusting a data receiver	438/200			Keeth, Brent	☑	☐	☐	☐	☐	☐	☐
3	☐	☐	US 20030137349 A1	20030724	17	System and method for a startup circuit for a differential CMOS amplifier	330/253			Sobel, David A.	☑	☐	☐	☐	☐	☐	☐
4	☐	☐	US 20030128071 A1	20030710	177	Variable transconductance variable gain amplifier utilizing a degenerated differential pair	330/254			Nguyen, Thinh Cat et al.	☑	☐	☐	☐	☐	☐	☐
5	☐	☐	US 20030056597 A1	20030327	15	Three-phase excitation circuit for compensated capacitor industrial process control transmitters	73/718			Wang, Rongtai	☑	☐	☐	☐	☐	☐	☐
6	☐	☐	US 20030030497 A1	20030213	155	Integrated VCO having an improved tuning range over process and temperature variations	331/34			Duncan, Ralph et al.	☑	☐	☐	☐	☐	☐	☐
7	☐	☐	US 20030020544 A1	20030130	126	Large gain range, high linearity, low noise MOS VGA	330/254			Behzad, Arya R.	☑	☐	☐	☐	☐	☐	☐
8	☐	☐	US 20020196057 A1	20021226	123	Data output circuit with reduced output noise	327/108			Hidaka, Hideto et al.	☑	☐	☐	☐	☐	☐	☐
9	☐	☐	US 20020175732 A1	20021128	10	Active auto zero circuit for programmable time continuous open loop amplifiers	327/307			Blon, Thomas et al.	☑	☐	☐	☐	☐	☐	☐
10	☐	☐	US 20020175731 A1	20021128	8	Active auto zero circuit for time continuous open loop amplifiers	327/307			Blon, Thomas et al.	☑	☐	☐	☐	☐	☐	☐
11	☐	☐	US 20020122344 A1	20020905	33	Sense amplifiers having reduced Vth deviation	365/205	257/E21.633; 257/E27.062; 257/E27.097		Takemura, Riichiro et al.	☑	☐	☐	☐	☐	☐	☐